

Amendments to the Drawings

Fig. 1 has been amended to include reference number 10.

Fig. 5B has been amended to replace reference number 181 with reference number 184.

Fig. 5C has been amended to replace reference number 181 with reference number 184, and to replace reference number 208 with reference number 205.

Attachment: Replacement Sheet

Annotated Marked-Up Drawings

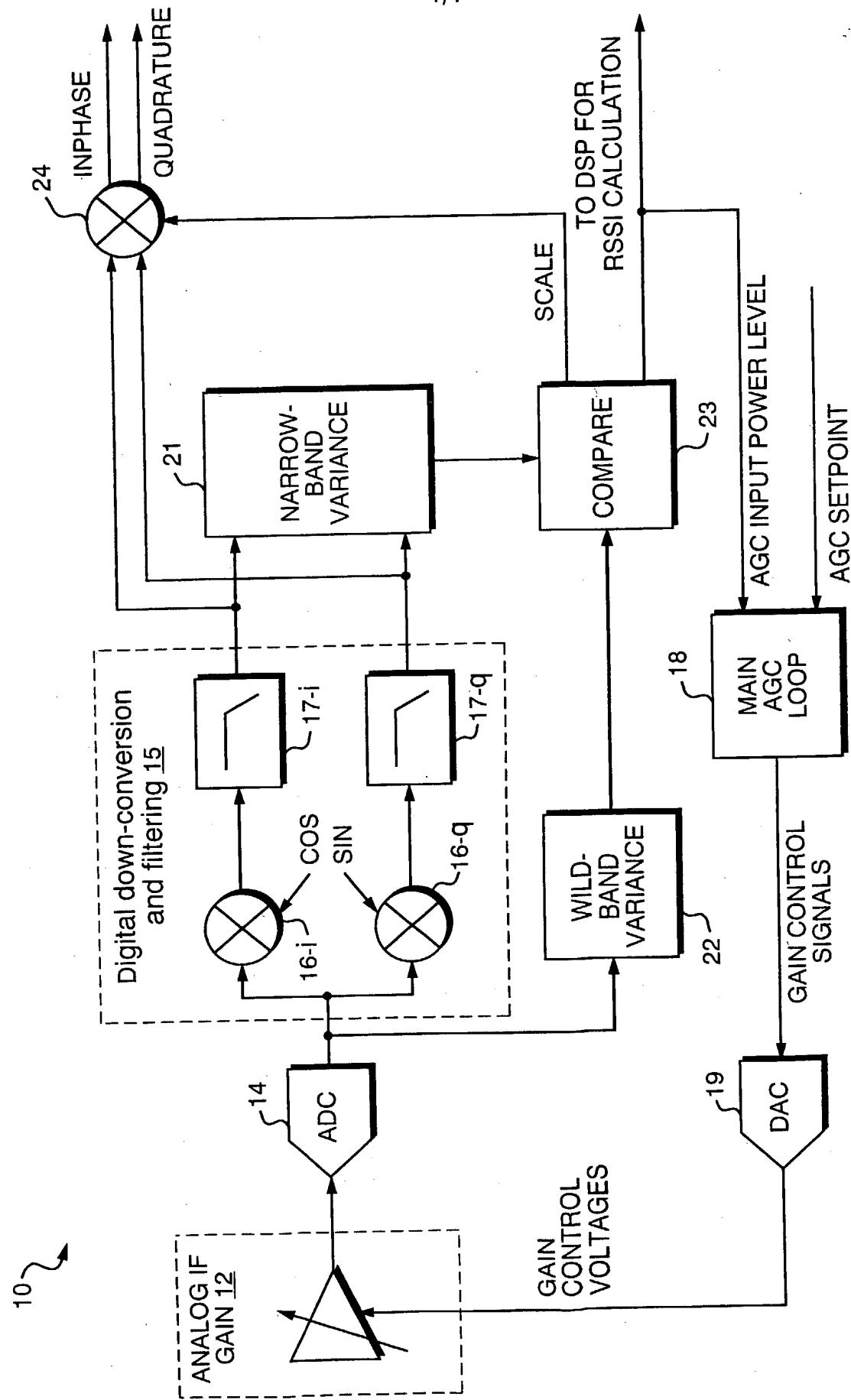
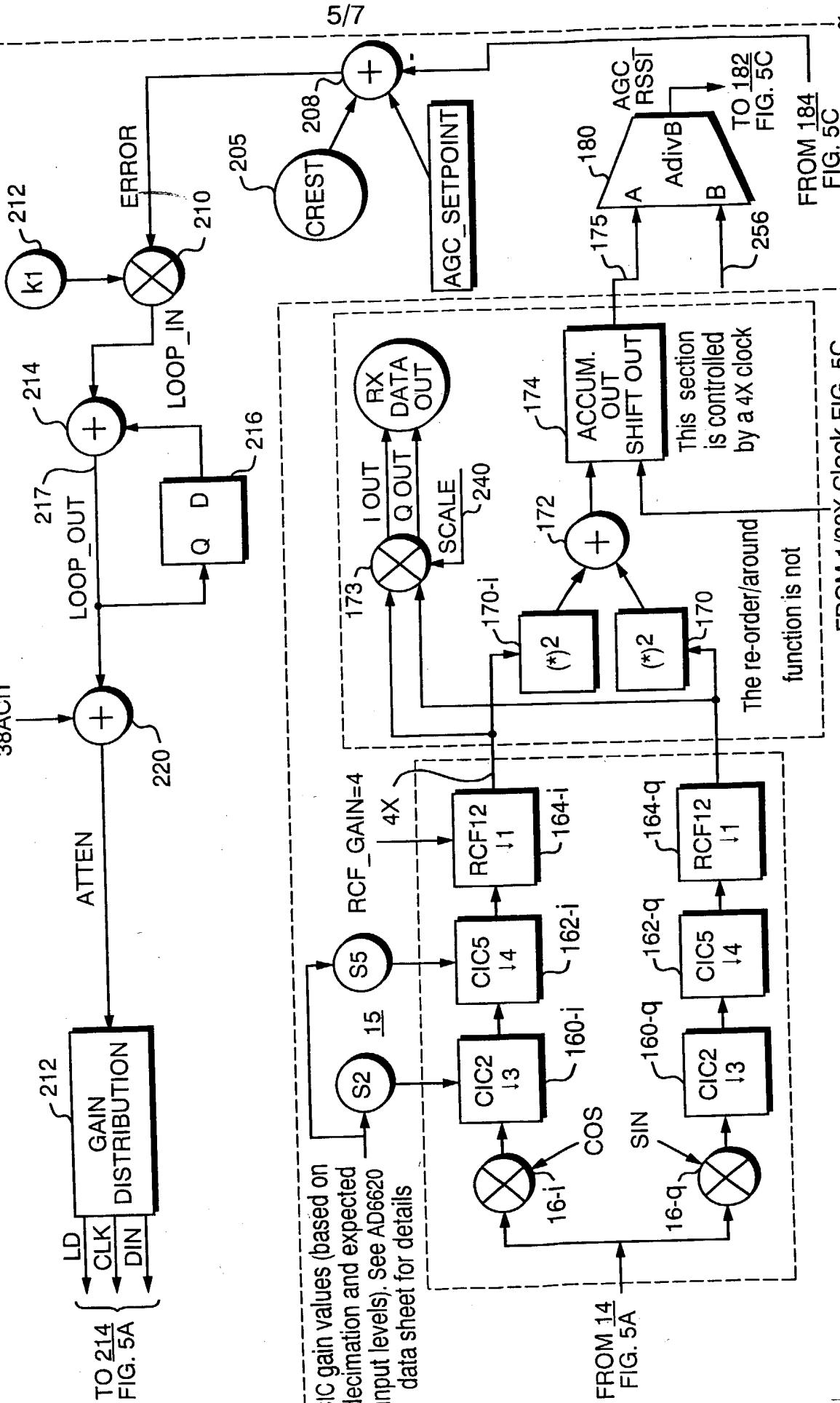


FIG. 1



This section is controlled by a 1/32X clock

Since the gain control voltage is linear-linear, filtering the RMS power in dB is possible. Otherwise, the data would need to be converted to volts prior to integrating.



CIC gain values (based on decimation and expected input levels). See AD6620 data sheet for details

FIG. 5B

